

IN THE CLAIMS:

1. (Currently Amended) A process for transmission of a message in a system, said process comprising the steps of sending, receiving, or propagating a sequence of packets having Interpacket gaps therebetween, each packet comprising a start-of-stream delimiter, and a series of at least 16 message bytes encoded in symbols uninterrupted by a control symbol, and wherein one of said Interpacket gaps comprises a plurality of symbols decoded as Idle symbols wherein said message is encoded in said one of said Interpacket gaps by replacing at least one of said plurality of symbols with a non-Idle symbol to form a modified Interpacket gap, such that the presence of said non-Idle symbol indicates a logic one or a logic zero is part of said message and said modified Interpacket gap complies with a packet transmission standard.

2. (Original) The process of claim 1 wherein said system comprises Fast Ethernet.

3. (Original) The process of claim 2 wherein said non-Idle symbol in said interpacket gap is the symbol for zero.

4. (Original) The process of claim 2 wherein said non-Idle symbol is a symbol having only one zero bit.

5. (Previously Presented) The process of claim 1 wherein said non-Idle symbol is based on said packet transmission standard.

6. (Currently Amended) The process of claim 1 wherein only a symbol for zero is used as said non-Idle symbol system comprises a Gigabit Ethernet and said non-Idle symbol comprises a K28.5/Dxx.y or K28.1/Dxx.y sequencee.

7. (Original) The process of claim 1 wherein said message comprises a side-channel.

8. (Previously Presented) A process for transmission of messages in a system, said process comprising the steps of sending, receiving, or propagating 1) more than one packet and 2) an interpacket gap, said packet comprising an information-carrying portion including at least 16 information bytes encoded in standard symbols between a start-of-packet delimiter and an end-of-packet delimiter, said message encoded in said information-carrying portion by adding at least one non-standard symbol, wherein said standard symbols are defined according to a packet transmission standard and said at least one non-standard symbol is classified as unused by said packet transmission standard.

9. (Original) The process of claim 8 wherein said interpacket gap includes both at least one symbol decoded as an Idle symbol and at least one non-Idle symbol such that the presence of said non-Idle symbol is part of a message.

10. (Original) The process of claim 9 wherein said system compromises Fast Ethernet.

11. (Original) The process of claim 10 wherein said non-Idle symbol is the symbol for zero.

12. (Original) The process of claim 10 wherein said non-Idle symbol is a symbol having only one zero bit.

13. (Original) The process of claim 9 wherein said system comprises Gigabit Ethernet.

14. (Original) The process of claim 13 wherein said non-Idle symbol comprises a K28.5/Dxx.y or K28.1/Dxx.y sequence.

Claims 15-20 (Canceled)

21. (Currently Amended) An apparatus, comprising:

a transmitter configured to transmit a signal having a plurality of packets and an interpacket gap, said interpacket gap having a plurality of symbols decoded as an Idle symbol, said Idle symbol defined according to an Ethernet standard, said transmitter including:

a buffer configured to store a message to be inserted into said interpacket gap;

a formatter configured to modify a bit stream representing said message to allow identification of message boundaries and to allow establishment of word alignment within said bit stream; and

an encoder configured to replace substitute at least one of said plurality of symbols decoded as an Idle symbol in said interpacket gap with a non-Idle symbol to form a modified interpacket gap, such that the presence of said non-Idle symbol [[is]] indicates a logic one or a logic zero at least a portion of said message and said modified interpacket gap complies with said Ethernet standard.

22. (Previously Presented) The apparatus of claim 21 wherein said formatter is configured to modify said bit stream with an HDLC flag.

23. (Previously Presented) The apparatus of claim 22 wherein said formatter is configured to insert a logic zero to said bit stream to avoid recognition of a portion of said message as said flag.

24. (Previously Presented) The apparatus of claim 21 wherein said Ethernet standard is Institute of Electrical and Electronic Engineers (IEEE) Standard 802.3.

25. (Currently Amended) The apparatus of claim 21 wherein only a single type of symbol is used as said non-Idle symbol at least one message symbol substituted by said encoder represents a

logic 1.

26. (Currently Amended) The apparatus of claim 25 ~~24~~ wherein said single type of symbol is a zero symbol at least one message symbol substituted by said encoder represents a logic 0.